

NIR Silicon Schottky Photodetector: from Metal to Graphene

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Abstract—In this work an advanced overview in the field of near-infrared silicon photodetectors, is presented. Proposed photodetectors are based on the internal photoemission effect through a Schottky junction and their fabrication results completely compatible with the silicon technology. Taking advantage of both new structures and new two-dimensional emerging materials, a progressive increase in device performance has been demonstrated along the last years. Our insights show that silicon devices based on the internal photoemission effect are already suitable for power monitoring applications and they could play a key role in the telecommunications opening new frontiers in the field of low-cost silicon photonics.

Keywords—internal photoemission effect, photodetector, Fabry-Perot, near-infrared, silicon.

I. INTRODUCTION

In order to develop all-silicon(Si) photodetectors (PDs) and to take advantage of low-cost standard Si-CMOS technology without additional material or process steps, a number of options have been proposed: two-photons absorption (TPA) [1], incorporation of optical dopants/defects with midbandgap energy levels into the Si lattice [1] and internal photoemission effect (IPE) [1]. The main advantages of these devices reside in their extremely large bandwidth and simple fabrication process, but, unfortunately, their efficiency is very low.

IPE is the optical excitation of electrons in the metal to energy above the Schottky barrier and then transport of these electrons to the conduction band of the semiconductor. The standard IPE theory is due to Fowler [2]. However, the Fowler's theory was obtained without taking into account the thickness of the Schottky metal layer. The enhancement of IPE in thin metal film was theoretically investigated by Vicker who introduced a multiplicative factor to the formula reported in the Fowler's work [3]. Finally, a further enhancement in IPE can be obtained due to the increase of the reverse voltage that lowers the Schottky barrier increasing the amount of emitted carriers [1]. Due to the very low signal-to-noise ratio, for a long time IPE-based Si PDs, at IR wavelengths, were believed usable only at cryogenic temperature. However, in order to enhance the photoemitted current with respect to the noise (dark) current, many structures have been proposed, they are

based on: surface plasmon polaritons (SPP) [4], micro- and nano-metric optical waveguide [5], optical microcavities [6,7].

II. NIR IPE-BASED SILICON PHOTODETECTORS

In this paragraph we have summarized our results of the last few years devoted to increase the performance of IPE-based Si PDs

A. IPE enhancement by an optical microcavity

In 2008, we experimentally demonstrated the influence of the optical microcavity on IPE for a Schottky Si junction [7]. In this work we proposed a device realized by a resonant cavity Fabry-Perot structure formed by a dielectric bottom mirror, a metallic top mirror and, in the middle, a silicon cavity (Fig. 1(a)). The dielectric bottom mirror is a distributed Bragg reflector (DBR) formed by alternating layers of amorphous hydrogenated silicon (a-Si:H) and silicon nitride (Si_3N_4) having $\lambda/4$ thicknesses, while, the top mirror was realized by a copper (Cu) layer working both as absorbing material and as optical mirror.

Responsivity measurements were carried out in the range of 1545–1558 nm (step of 0.05 nm). Fig. 1(b) shows the room temperature responsivity versus the wavelength for two devices: the first one provided with a bottom mirror reflectivity (R_{BM}) of 0.3, (realized by a simple Si/air interface), and the second one provided with a bottom mirror reflectivity (R_{BM}) of 0.99 (realized by a DBR with five pairs of $\text{Si}_3\text{N}_4/\text{a-Si:H}$), respectively. In both cases it is possible to observe four peaks. The measured free spectral range of 3.3 nm is in a perfect agreement with the cavity thickness (100 μm). By looking at device having $R_{\text{BM}}=0.3$, a measured cavity finesse F and responsivity of 2.9 and 1.6 $\mu\text{A/W}$, were respectively reported. On the other hand, concerning device having $R_{\text{BM}}=0.99$, a cavity finesse F and responsivity of 4.7 and 4.3 $\mu\text{A/W}$, were respectively reported showing a responsivity enhancement of about three times due to the increased finesse. Then in the 2010 we proved that a further increase in responsivity (about 8 μA) could be obtained by reducing the size of the device [8]. Finally, in 2012 we proved that a significant increase in responsivity of 0.063 mA/W could be obtained by fulfilling the so-called critical coupling conditions (Fig. 2(a) and 2(b)) [9].

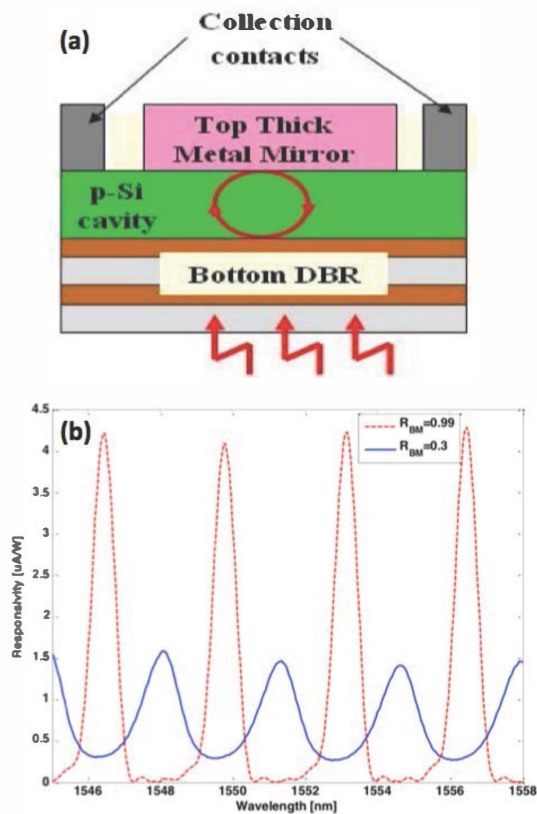


Figure 1. PD reported in ref. [7]: (a) Schematic cross section and (b) responsivity vs wavelength.

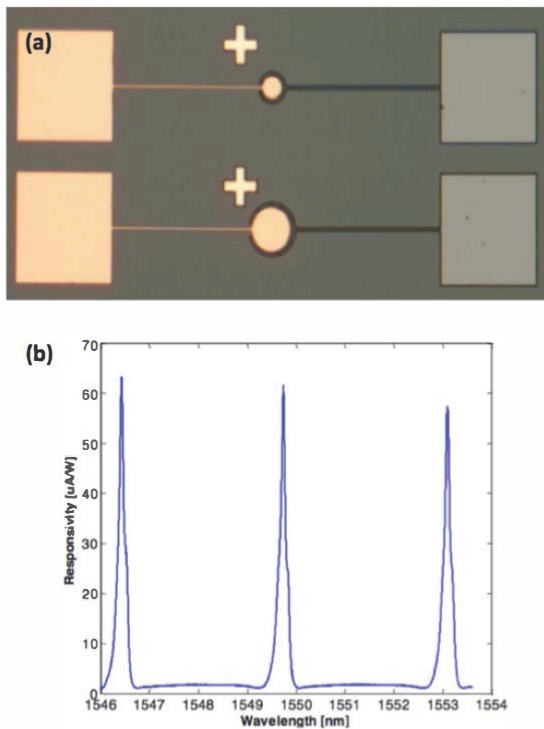
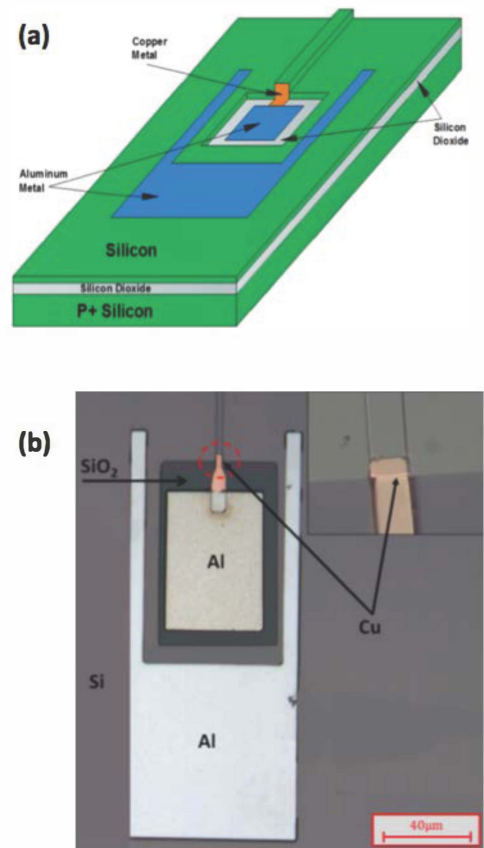


Figure 2. PD proposed in ref. [9]: (a) PD top view and (b) responsivity vs wavelength.

B. IPE enhancement by a waveguide structure

It is well known from IPE theory that lowering the potential barrier can increase emission of photogenerated carriers into silicon. It can be obtained by applying a reverse bias to the Schottky junction [1], but, unfortunately, the higher the reverse voltage, the higher the dark current. Increase in dark current could be limited by reducing the Cu active area in contact with Si but, in this case, the light coupling on a small size of absorbing layer is not a trivial task to reach. A guiding structure can be used in order to overcome this drawback. Very recently we have proposed a silicon-on-insulator (SOI) waveguide asymmetric metal-semiconductor-metal (MSM) PD based on IPE and working at 1550 nm [10]. The main novelty of the structure is that the absorbing metal (Cu) is in contact with silicon only on the vertical wall of the optical waveguide (Fig. 3(a) and (b)). The main advantage is that PD is able to tackle typical responsivity/dark current trade-off afflicting IPE-based devices performances. Taking advantage of the small contact area of about $3 \mu\text{m}^2$ it has been possible to increase the reverse applied voltage up to 21 V with a very low dark current of 2.2 nA. In addition an increased responsivity of 4.5 mA/W (Fig.3(c)), and a bandwidth of 1 GHz (Fig.3(d)) have been demonstrated. Finally, device exhibits the capability to extend its wavelength cut-off to the mid-infrared regime under an applied reverse bias [10].



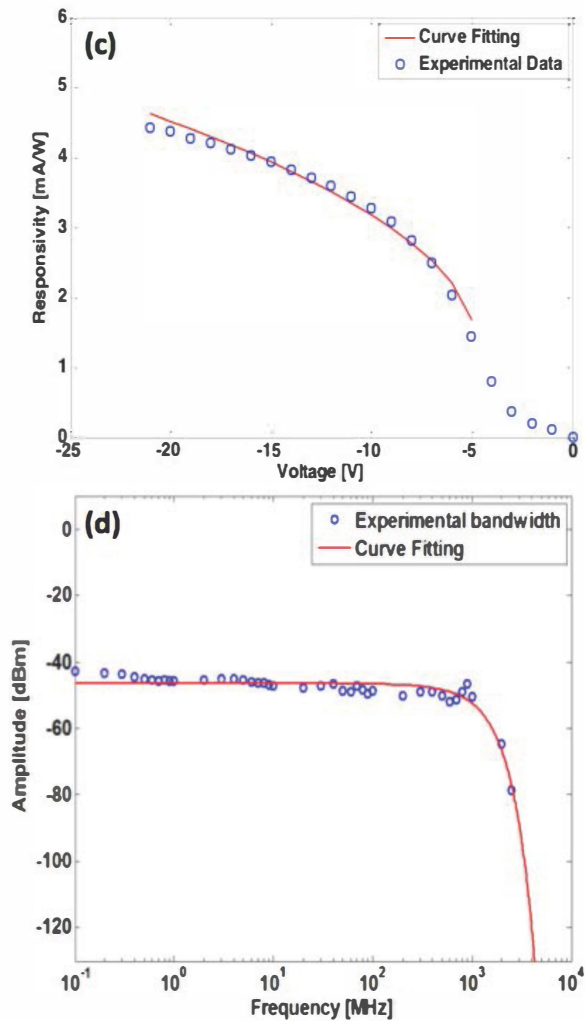


Figure 3. PD proposed in ref. [10]: (a) sketch, (b) top view, (c) responsivity vs reverse voltage and (d) bandwidth.

C. IPE enhancement by two-dimensional materials (graphene)

In a recent paper Amirmazlaghani *et al.* [11] show that canonical Fowler's theory is not suitable to describe a Schottky junction in which a two-dimensional layer plays the role of the metal. The main reason is that, due to the two dimensionality of the material, the photoexcited carriers have more chance to gain the right momentum to overcome the Schottky barrier providing a significantly increase in responsivity. Graphene is a two-dimensional emerging material that is able to form Schottky junction on Si [12] and due to its excellent electrical properties it is the best option to realize IPE-based devices. Unfortunately due to its low absorption (2.3%) [13] only a limited responsivity can be expected, but, by introducing the Schottky junction inside a Fabry-Perot microcavity a significant increase in responsivity can be obtained. We have fabricated a Fabry-Perot vertical-to-the-surface structure formed by a bottom and a top metal mirror surrounding a $\lambda/2$ silicon layer. The air-silicon and

graphene/silicon interfaces act as bottom and top mirror, respectively. The samples have been fabricated starting from a quasi-intrinsic bi-polished 100- μm -thick silicon wafer. Substrate has been chosen quasi-intrinsic in order to avoid free carrier absorption. Graphene has been grown on copper foil by the chemical vapour deposition (CVD) method and then transferred on Si. A maximum responsivity of about 0.08 mA/W at -5 V around 1550 nm, has been experimentally demonstrated. This value is more than one order of magnitude higher than that reported in Fig. 1(b) (blue solid line) for a similar device but realized with Cu. Theory of IPE through a graphene/Si junction predicts an increase in efficiency of more than two orders of magnitude, discrepancy with experimental results could be due to the presence of defects on graphene linked to the presence of residual PMMA. IPE-based PDs performance are still suitable for power monitoring applications, however, it is our idea that the optimization of both graphene deposition and the resonant structure could open the path to the realization of devices with responsivity in the range of A/W usable in the telecommunications.

III. CONCLUSIONS

In this work we have reported the advances of our activity in the field of Si PDs based on IPE working at 1550 nm and at room temperature. The best performance of our device in term of responsivity, bandwidth and dark current are 4.5 mA/W, 1 GHz and 2.2 nA, respectively. Our results prove that these devices are already suitable for power monitoring applications; in addition, new perspectives seem to be opened by the rise of new two-dimensional material like graphene. It is our opinion that this material combined to the fabrication of new structures will push IPE-based Si PDs to play a key role in the telecommunications, opening new frontiers in the field of low-cost silicon photonic.

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